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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/585,864	06/01/2000	Wen Li	4309US (99-1328)	4732
7	590 09/07/2004		EXAMINER	
Joseph A Walkowski			BURD, KEVIN MICHAEL	
Trask Britt P O Box 2550			ART UNIT	PAPER NUMBER
Salt Lake City, UT 84110			2631	

Please find below and/or attached an Office communication concerning this application or proceeding.

			MI.
	Application No.	Applicant(s)	
A A A A A A A A A A A A A A A A A A A	09/585,864	LI ET AL.	•
Office Action Summary	Examiner	Art Unit	
	Kevin M. Burd	2631	
The MAILING DATE of this communication Period for Reply	appears on the cover sl	neet with the correspondence ad	dress
A SHORTENED STATUTORY PERIOD FOR RE THE MAILING DATE OF THIS COMMUNICATIO - Extensions of time may be available under the provisions of 37 CFF after SIX (6) MONTHS from the mailing date of this communication - If the period for reply specified above is less than thirty (30) days, a - If NO period for reply is specified above, the maximum statutory pe - Failure to reply within the set or extended period for reply will, by st Any reply received by the Office later than three months after the m earned patent term adjustment. See 37 CFR 1.704(b).	N. R 1.136(a). In no event, however reply within the statutory minimu riod will apply and will expire SIX atute. cause the application to be	may a reply be timely filed m of thirty (30) days will be considered timel (6) MONTHS from the mailing date of this occome ABANDONED (35 U.S.C. § 133)	y. ommunication.
Status			
1) Responsive to communication(s) filed on 1	8 June 2004.		•
	This action is non-final.		
3) Since this application is in condition for allo closed in accordance with the practice unde	wance except for forma		merits is
Disposition of Claims			
4)⊠ Claim(s) <u>25-41</u> is/are pending in the applica	ation.	*	
4a) Of the above claim(s) is/are without		on.	
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>25-41</u> is/are rejected.	•	•	
7) Claim(s) is/are objected to.	•		
8) Claim(s) are subject to restriction an	d/or election requireme	nt.	
Application Papers			
9)☐ The specification is objected to by the Exam	iner.		
10)⊠ The drawing(s) filed on <u>18 June 2004</u> is/are:	: a)⊠ accepted or b)□	objected to by the Examiner.	
Applicant may not request that any objection to		-	
Replacement drawing sheet(s) including the con			
11)☐ The oath or declaration is objected to by the	Examiner. Note the att	ached Office Action or form PT	O-152.
Priority under 35 U.S.C. § 119	* .*		
12) Acknowledgment is made of a claim for fore	ign priority under 35 U.	S.C. § 119(a)-(d) or (f).	
a)☐ All b)☐ Some * c)☐ None of:			
1. Certified copies of the priority docume			
2. Certified copies of the priority docume		·	
3. ☐ Copies of the certified copies of the p		-	Stage
application from the International Bur	, , , ,		
* See the attached detailed Office action for a l	ist of the certified copie	s not received.	
			7.
Attachment(s)	•		
1) Notice of References Cited (PTO-892)		rview Summary (PTO-413)	, .
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Pap	er No(s)/Mail Date	450)
Information Disclosure Statement(s) (PTO-1449 or PTO/SB/Paper No(s)/Mail Date	08) 5) 1 Noti	ce of Informal Patent Application (PTO er:	-102)
J.S. Patent and Trademark Office PTOL-326 (Rev. 1-04) Office	Action Summary	Part of Paper No	/Mail Date 7



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1. This office action, in response to the amendment filed 6/19/2004, is a non-final office action.

Response to Amendment

- 2. The previous objection to the abstract is withdrawn.
- 3. The drawings were received on 6/19/2004. These drawings are approved.
- 4. In view of the addition of new claims 1-41, rejections of these claims are stated below.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 5. Claims 25-41 are rejected under 35 U.S.C. 102(e) as being anticipated by lee et al (US 6,377,093).

Regarding claims 25, 33, 36, 40 and 41, Lee discloses a method and apparatus for generating delay locked clocks. Figure 10 discloses determining a phase difference in phase detector 1003 between a clock signal ECLK and a first delayed clock signal

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FCLK12. The phase detector outputs a signal to control the delay of delay unit 1003 (column 4, lines 33-36). A second phase difference is determined between an inverse clock signal FCLK12 and a second delayed clock signal ICLK2 in TDC 1011 (column (column 5, lines 37-40). FCLK12 is an inverse clock signal because for any clock signal there exists an inverse of that clock signal. This phase difference signal is used to control the variable delay circuit 1017 (column 10, lines 25-27). The circuit shown in figure 10 is found on an integrated circuit (abstract) and is used in SDRAMs (column 1, lines 14-19) that are found in computing systems.

Regarding claims 26-29, 32, 34, 35 and 37, rising and failing edges of the signal are generated according to the comparison of the phase values. This changes the delay in the delay circuits and the corresponding phase.

Regarding claims 30 and 31, when the phase differences are zero, no changes to the delay circuits take place.

Regarding claims 38 and 39, the first delay unit 1003 is coupled through a third delay circuit 1007 in figure 10.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin M. Burd whose telephone number is (571) 272-3008. The examiner can normally be reached on Monday - Thursday 9 am - 5 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammad Ghayour can be reached on (571) 272-3021. The fax phone

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number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kevin M. Burd

9/5/2004